

L Number	Hits	Search Text	DB	Time stamp
-	27	integrat\$3 adj controller ADJ CHIP	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:48
-	0	((integrat\$3 adj controller ADJ CHIP) AND GRAPHIC adj CONTROLLER	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:48
-	0	((integrat\$3 adj controller ADJ CHIP) AND (GRAPHIC ADJ CONTROLLER)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:48
-	5196	GRAPHIC ADJ CONTROLLER	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:48
-	2898	(GRAPHIC ADJ CONTROLLER) AND INTEGRA\$5	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:48
-	989	((GRAPHIC ADJ CONTROLLER) AND INTEGRA\$5) AND data adj process\$4	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:48
-	182	((((GRAPHIC ADJ CONTROLLER) AND INTEGRA\$5) AND data adj process\$4) and (interfac\$5 adj bus\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:49
-	124	PCMCIA ADJ ADAPT\$3	USPAT	2004/06/28 04:49
-	28	((((GRAPHIC ADJ CONTROLLER) AND INTEGRA\$5) AND data adj process\$4) and (interfac\$5 adj bus\$3)) and (digital adj signal)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:49
-	14	((((integrat\$3 adj controller) and cpu) and (digital adj signal)) and (data adj interfac\$5)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:49
-	4	((((integrat\$3 adj controller) and cpu) and (digital adj signal)) and (interfac\$5 adj bus\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:49
-	19	((integrat\$3 adj controller) and cpu) and (interfac\$5 adj bus\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:49
-	14	(data adj process\$4 WITH (interfac\$5 adj bus\$3)) and (digital adj signal)	USPAT	2004/06/28 04:49
-	22	((data adj process\$4 WITH (interfac\$5 adj bus\$3)) and controller) and analog	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:49
-	18	(((((GRAPHIC ADJ CONTROLLER) AND INTEGRA\$5) AND data adj process\$4) and (interfac\$5 adj bus\$3)) and (digital adj signal)	USPAT	2004/06/28 04:49
-	12	(PCMCIA ADJ ADAPT\$3) AND (VIDEO ADJ MEMORY)	USPAT	2004/06/28 04:49
-	34	(integrat\$3 adj video) adj memor\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:49
-	1	("6002411").PN.	USPAT	2004/06/28 04:49
-	2	((("5650955") or ("5703806"))).PN.	USPAT	2004/06/28 04:49
-	2	"20020149560"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 04:49

-	5	((("6356497") or ("6041010") or ("5650955") or ("5703806")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 05:10
-	5998	MOS adj2 integrated adj circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 06:50
-	566	(MOS adj2 integrated adj circuit) and pmos	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 06:51
-	514	((MOS adj2 integrated adj circuit) and pmos) and nmos	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 06:51
-	82	((((MOS adj2 integrated adj circuit) and pmos) and nmos) and logic adj gate	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 06:51
-	5	(((((MOS adj2 integrated adj circuit) and pmos) and nmos) and logic adj gate) and reverse adj1 bias\$3	USPAT; US-PGPUB; EPO; JPO; DERWENT	2004/06/28 06:52